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1784

JOHN

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

|                      |                           |              |                 |             |                       |
|----------------------|---------------------------|--------------|-----------------|-------------|-----------------------|
| APPL NUM<br>10056770 | FILING DATE<br>01/24/2002 | CLASS<br>219 | SUBCLASS<br>703 | GAU<br>1752 | EXAMINER<br>LONG TRAN |
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\*\*APPLICANTS: Ferber Gottfried; Pelmer Reimund;

\*\*CONTINUING DATA VERIFIED: ✓ UST

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\*\* FOREIGN APPLICATIONS VERIFIED:

GERMANY 101 03 084.3 01/24/2001 ✓ UST

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|---|--|-----------------------------------|
| PG-PUB  | DO NOT PUBLISH <input type="checkbox"/>  | RESCIND <input type="checkbox"/>  |
| Foreign priority claimed<br>35 USC 119 conditions met<br>Verified and Acknowledged Examiners's initials | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no<br><input checked="" type="checkbox"/> yes <input type="checkbox"/> no<br>LK | ATTORNEY DOCKET NO<br>WMP-EUP-008 |
| TITLE : Semiconductor module and method for fabricating the semiconductor module                        |  |                                   |
| U.S. DEPT. OF COMM /PAT & TM-PTO-436L (Rev. 12-54)  |  |                                   |

|                            |           |  |                |                      |
|----------------------------|-----------|--|----------------|----------------------|
| NOTICE OF ALLOWANCE MAILED |           | Assistant Examiner   | CLAIMS ALLOWED |                      |
|                            |           |  | Total Claims   | Print Claim for O.G. |
| ISSUE FEE                  |           | Primary Examiner   | DRAWING        |                      |
| Amount Due                 | Date Paid |  | Sheets Drwg.   | Figs.Drwg.           |
| TERMINAL                   |           | PREPARED FOR ISSUE   |                | Application Examiner |
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